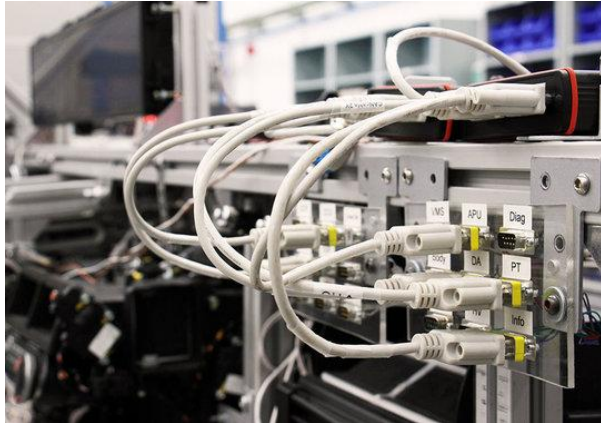


Electrical and Electronics Architecture



DESCRIPTION

Applus+IDIADA provides comprehensive, cost effective and efficient solutions to fulfil our clients' requirements:

- Requirements capture and analysis
 - EE features list
 - Development process and tool chain
- Features analysis
 - Function definition and simulation
 - Partitioning of distributed functions
 - Function/systems circuit diagrams
- Power management and distribution
 - Fuse and ground strategies
 - Power mode strategy
 - Quiescent current analysis
- Definition of vehicle network topology
 - Communication networks (CAN, LIN...)
 - Bus load
 - Wake-up strategies (CAN, wired and wireless)
- Wiring harness benchmark, design and development
- Component and system integration